## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Jung-a Choi EXAMINER: Matthew E. Warren

SERIAL NO.: 10/590,101 GROUP ART UNIT: 2815

FILED: July 25, 2007 Attorney Docket: 8021-408 (SS-21261-US)

FOR: SEMICONDUCTOR DEVICE HAVING MULTI-GATE STRUCTURE

AND METHOD OF MANUFACTURING THE SAME

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

## RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Office Action dated October 26, 2009, applicant provisionally elects for prosecution Group I, claims 1-18, for prosecution.

Groups I and II may be distinct as set forth by the Examiner, Applicants believe that simultaneous examination will not present an undue burden. Under such circumstances, the Examiner is encouraged to maintain the claims of Groups I and II in the same application. See, MPEP § 803.

Early and favorable consideration of the application is earnestly solicited.

Respectfully submitted,

Frank Chau Reg. No. 34,136

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